

SBAC-PAD 2015

27th International Symposium on Computer Architecture and High Performance Computing

October 18-21, 2015

Majestic Palace Hotel, Florianópolis, Santa Catarina, Brazil

General Chair

Mario A. R. Dantas
Federal University of Santa Catarina

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Viktor K. Prasanna
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Track Chairs

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Architecture
David R. Kaeli - *Northeastern University*
Software
Albert Cohen - *INRIA*
Distributed Systems and Networks
Dilma Da Silva - *Texas A&M University*

Conference Website

<http://www.sbac-pad.org>

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About SBAC-PAD

SBAC-PAD is an international annual symposium, started in 1987, which has continuously presented an overview of new developments, applications, and trends in parallel and distributed computing technologies. SBAC-PAD is open for faculty members, researchers, specialists and graduate students around the world.

In this edition, the symposium will be held at the Majestic Palace Hotel, Florianópolis. Known as "the Island of Magic", Florianópolis is the favorite destination for tourism in the Southern part of Brazil. The city is known by its 42 beautiful beaches, special resorts, historical buildings, environmental protected areas, and by an economy strongly based on information technology.

Authors are invited to submit manuscripts on all aspects of computer architecture and high performance computing. Authors of selected papers will be invited to submit extended versions of their papers for publication on the Journal of Parallel and Distributed Computing. Topics of interest include (but are not limited to):

- Application-specific systems
- Benchmarking, performance measurements, and analysis
- Cloud, Grid, cluster, and peer-to-peer systems
- Embedded and pervasive systems
- GPUs, FPGAs and accelerator architectures
- Languages, compilers, and tools for parallel and distributed programming
- Modeling and simulation methodology
- Operating systems and virtualization
- Parallel and distributed systems, algorithms, and applications
- Power and energy-efficient systems
- Processor, cache, memory, storage, and network architecture
- Real-world applications and case studies
- Reconfigurable, resilient and fault-tolerant systems

Submissions must be in English, 8 pages maximum, following the IEEE conference formatting guidelines. To be published in the SBAC-PAD 2015 Conference Proceedings and to be eligible for publication at the IEEE Xplore, one of the authors must register at the full rate.

Important Dates

Abstract deadline:	June 1 st
Paper deadline:	June 8 th
Rebuttal deadline:	July 9 th
Author notification:	July 19 th
Camera ready:	August 14 th