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Call for Papers**SBAC-PAD 2005**
**17th International Symposium on Computer Architecture
 and High Performance Computing
 Copacabana, Rio de Janeiro, October 24-27, 2005**
<http://www.sbc.org.br/sbac/2005>

Held annually since 1987, SBAC-PAD is an international conference that traditionally presents an overview of new developments and trends in computer architecture, parallel and distributed technologies and applications for high performance computing. This year, the symposium will be held in Hotel Pestana, Rio de Janeiro, Brazil.

SBAC-PAD welcomes and invites practioners, researchers, developers, faculty members and students around the world to submit manuscripts on all aspects of computer architecture and high performance computing. Topics of interest include (but are not limited to):

- Application-specific Architectures;
- Cache and Memory Architectures;
- Fault-Tolerant Architectures and Systems;
- Grid, Cluster, Pervasive and Heterogeneous Computing;
- High Performance Applications;
- Languages, Compilers and Tools for Parallel and Distributed Programming;
- Load Balancing and Scheduling;
- Operating Systems;
- Processor Microarchitectures;
- Parallel and Distributed Algorithms, Architectures, Interconnection Networks, Routing and Communication;
- Benchmarking, Performance Measurements and Analysis;
- Reconfigurable Systems.

Manuscripts of original research must be written in English, not exceeding 8 pages in double column using the IEEE style, and be in a PDF or Postscript format. Please check the symposium homepage for more detailed information regarding paper format and electronic submission.

All submitted papers will be reviewed internationally for quality, correctness, originality and relevance. At least one of the authors of accepted papers must register and present the paper at the conference. Simultaneous submission to other journals or conferences with published proceedings is not allowed.

Once again the best paper award in the area of Applications in High-Performance Systems will be sponsored by NEC do Brasil. Furthermore, authors of selected high quality papers will be invited to submit revised and extended versions for publication in the International Journal of High Performance Computing and Networking (<http://www.ijhpcn.org/>).

Proposals for high-quality tutorials in all SBAC-PAD related areas, from academic research to industrial applications, are solicited. Prospective authors of tutorials are invited to submit a preliminary proposal of a two-hour presentation to the Tutorial Chair (Roseli S. Wedemann, roseli@ime.uerj.br), including title, brief description of topics to be covered, and a short biography of the speakers.

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Important Dates:

Paper submission: June 2, 2005
(New Extended Deadline)
Author notification: July 4, 2005
Camera-ready copy: July 25, 2005
Tutorial proposals: August 1, 2005
Tutorial notification: August 22, 2005

Promoters:**Organizers:****SBAC-PAD 2005 will be held in conjunction with:**